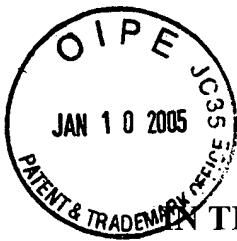


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Attorney Docket No.: 0180164

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78/3  
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## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: **Ramsbey, et al.**

Serial No.: 09/430,366

Filed: October 28, 1999

For: **Method of Making a Memory Cell  
with Polished Insulator Layer**

Art Unit: 2813

Examiner: Chen, Jack S. J.

**RESPONSE TO PTO NOTICE REGARDING DRAWINGS**

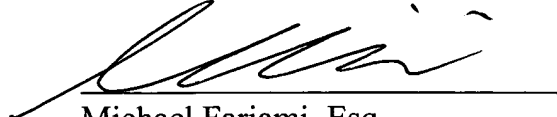
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the PTO Notice Regarding Drawings dated December 22, 2004 in the above-referenced patent application. Applicant has amended Figures 1-7 as required by Notice of Draftsperson's Patent Drawing Review (Form PTO-948), dated December 22, 2004, and has enclosed two (2) replacement sheets including Figures 1-7 and two (2) unmarked formal drawing sheets including Figures 1-7. Issue fee in the present application was paid on October 19, 2004. As such, an early issuance of the present application is respectfully requested.

Respectfully Submitted,  
FARJAMI & FARJAMI LLP

Date: 1/5/05

  
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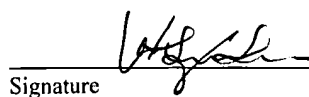
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Date

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